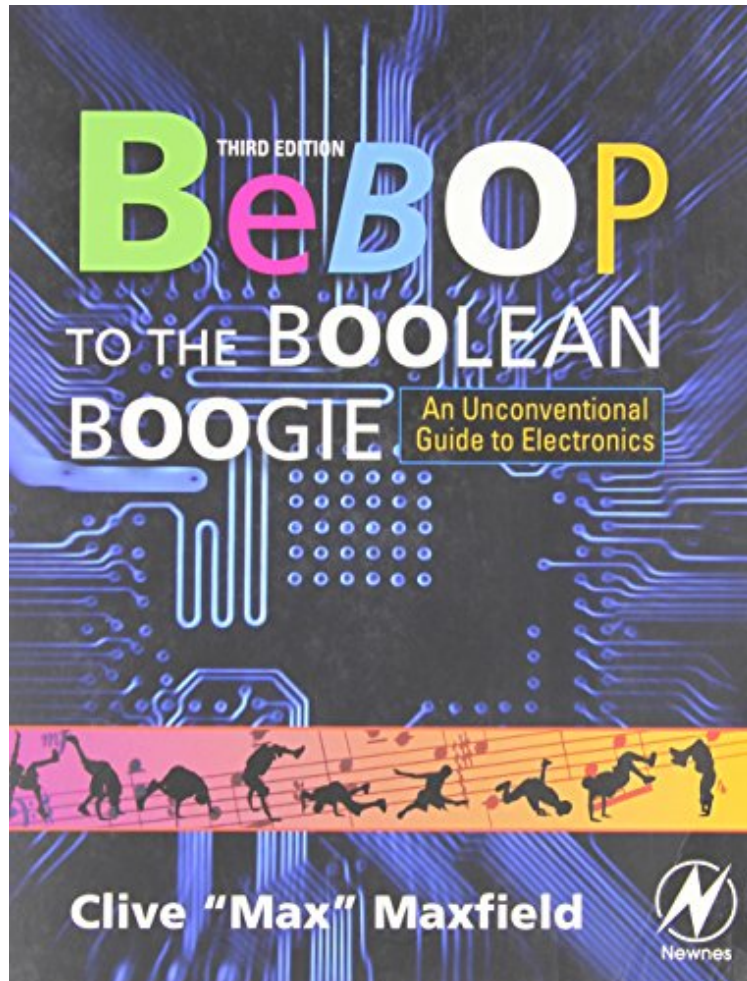


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CONTENTS:Section 1: FundamentalsChapter 1 Analog versus Digital Chapter 2 Atoms, Molecules, and Crystals Chapter 3 Conductors, Insulators, and Other Stuff Chapter 4 Semiconductors (Diodes and Transistors) Chapter 5 Primitive Logic Functions Chapter 6 Using Transistors to Build Logic Gates Chapter 7 Alternative Numbering Systems Chapter 8 Binary Arithmetic Chapter 9 Boolean Algebra Chapter 10 Karnaugh Maps Chapter 11 Slightly More Complex Functions Chapter 12 State Machines Chapter 13 Analog-to-Digital and Vice VersaSection 2: Components and ProcessesChapter 14 Integrated Circuits (ICs) Chapter 15 Memory ICs Chapter 16 Programmable ICs Chapter 17 Application-Specific Integrated Circuits (ASICs) Chapter 18 Circuit Boards Chapter 19 Hybrids Chapter 20 System-in-Package (Sip) and FriendsChapter 21 Alternative and Future Technologies Section 3: Design Tools and StuffChapter 22 General Concepts Chapter 23 Design and Verification Tools Appendix A Assertion-Level Logic Appendix B Positive Logic versus Negative Logic Appendix C Reed-Miller Logic Appendix D Gray Codes Appendix E Linear Feedback Shift Registers (LFSRs) Appendix F Pass-Transistor Logic Appendix G More on Semiconductors Appendix H Rounding Algorithms 101 Appendix I Pass-Transistor Logic Appendix J An Interesting Conundrum Abbreviations and Acronyms Glossary Index

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About the AuthorClive "Max" Maxfield received a BS in Control Engineering from Sheffield Polytechnic, England in 1980. He began his career as a mainframe CPU designer for International Computers Limited (ICL) in Manchester, England. Max now finds himself a member of the technical staff (MTS) at Intergraph Electronics, Huntsville, Alabama. Max is the author of dozens of articles and papers appearing in magazines and at technical conferences around the world. Max's main area of interest are currently focused in the analog, digital, and mixed-signal simulation of integrated circuits and multichip modules.